FIG. 1

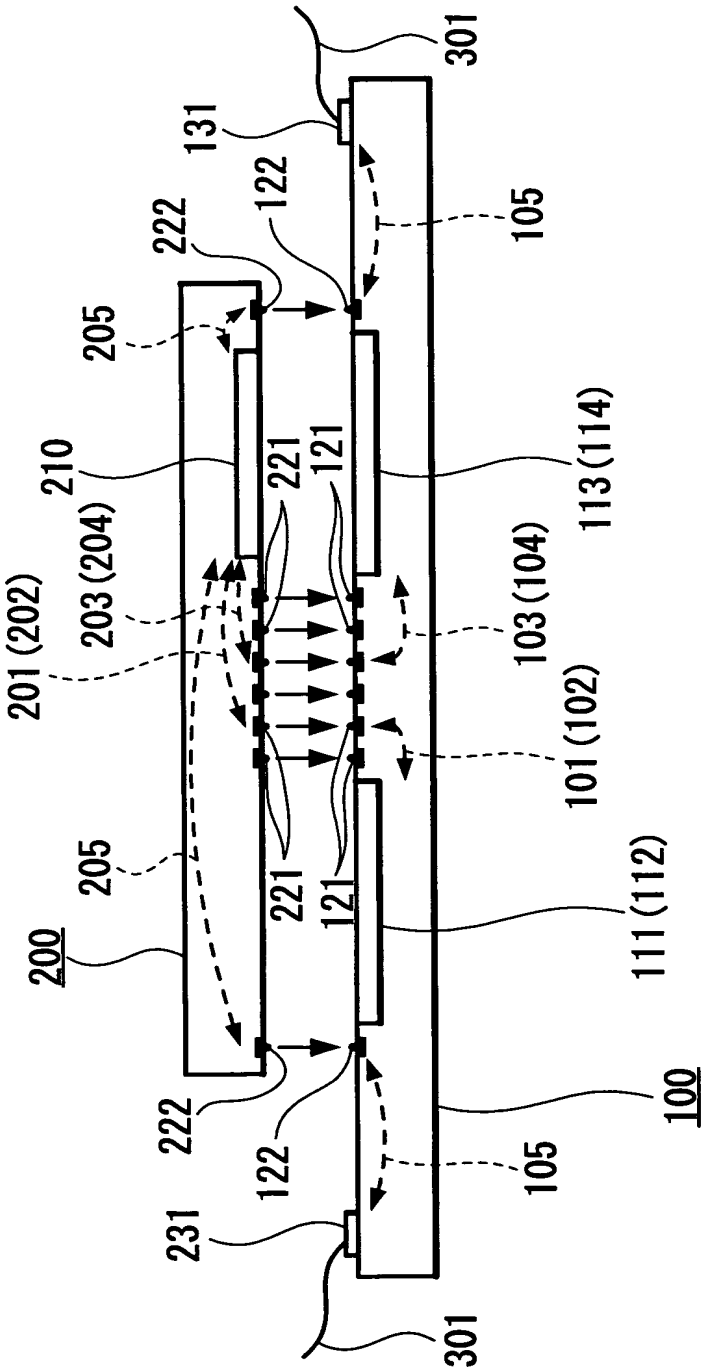
301
131
222
122
105
205
210
221
121
113 (114)
203 (204)
201 (202)
103 (104)
101 (102)
205
200
221
121
111 (112)
222
122
105
100
231
301

FIG. 2

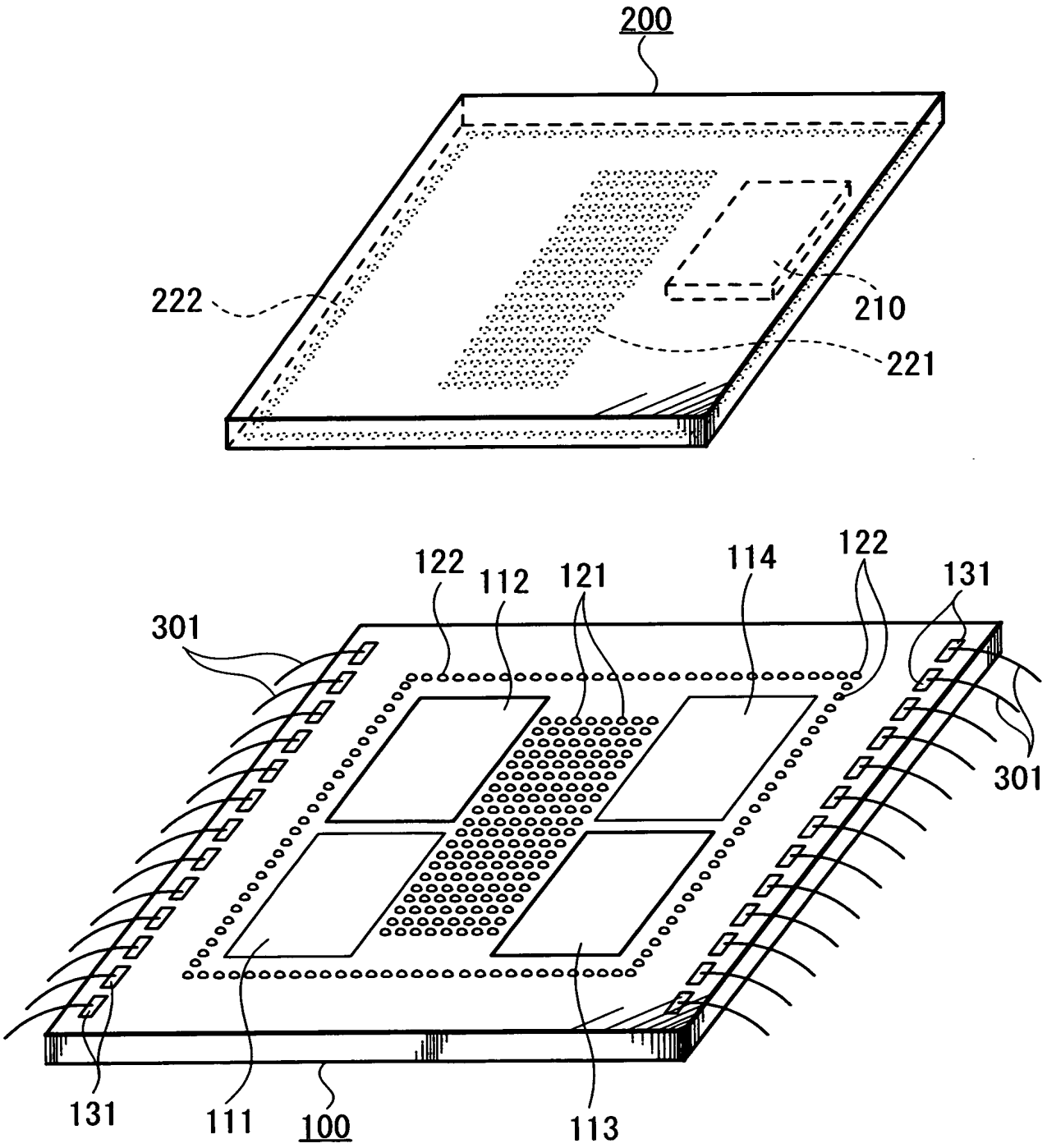

FIG. 3

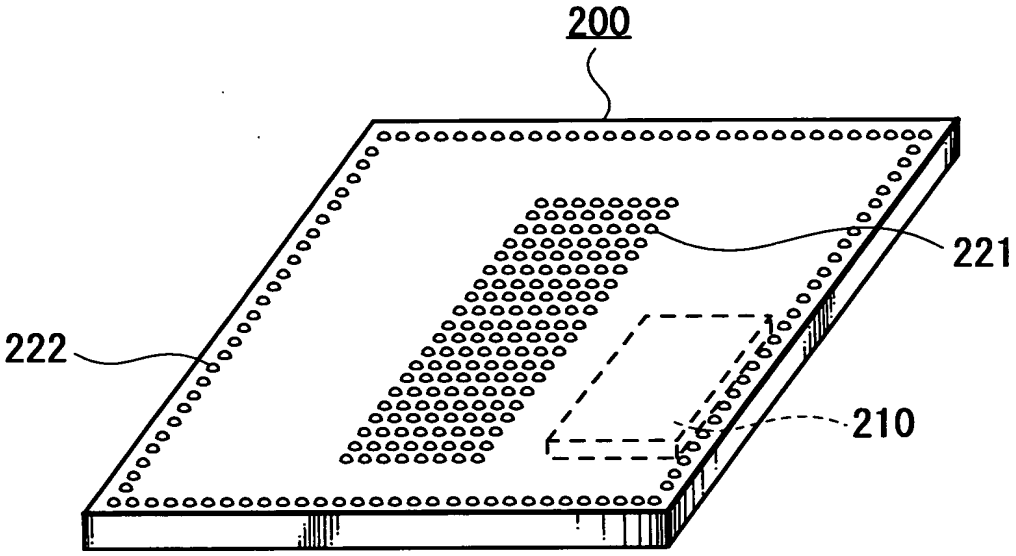

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FIG. 4

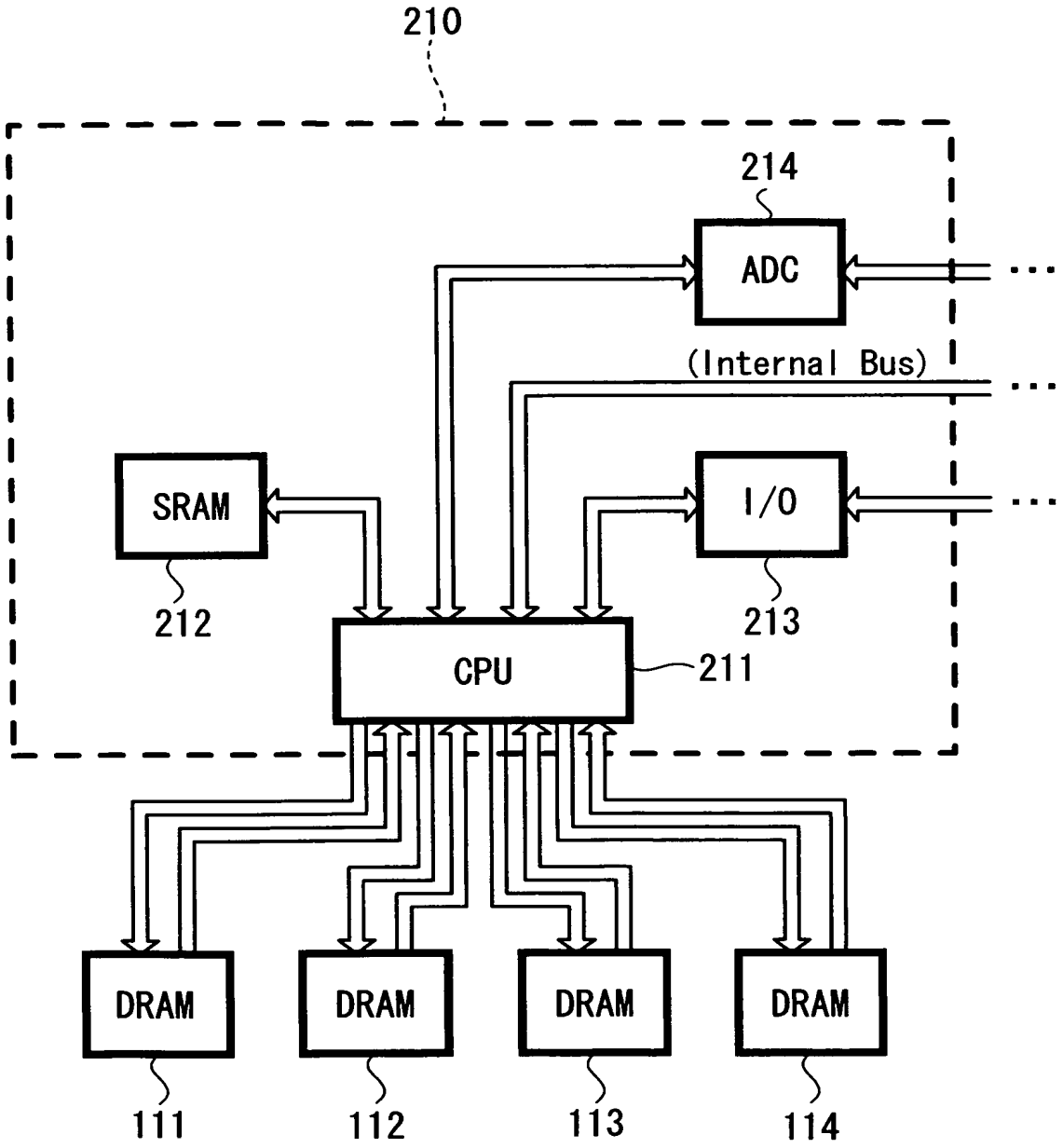


FIG. 5

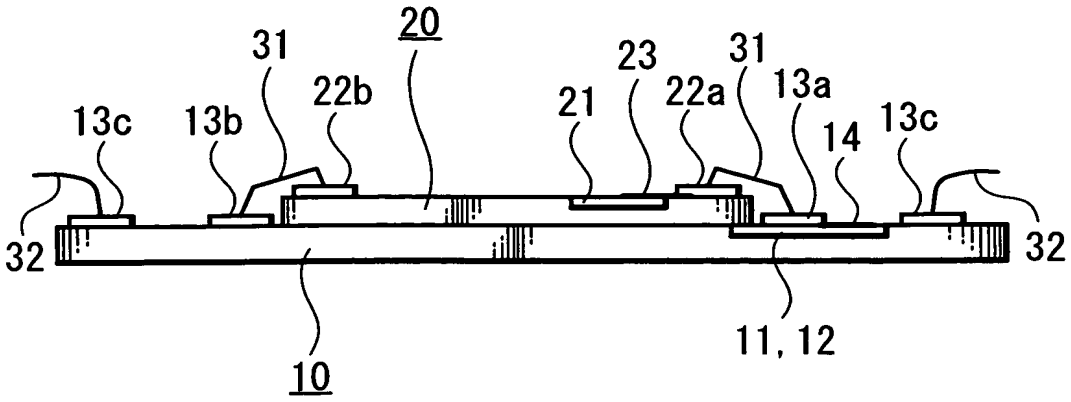
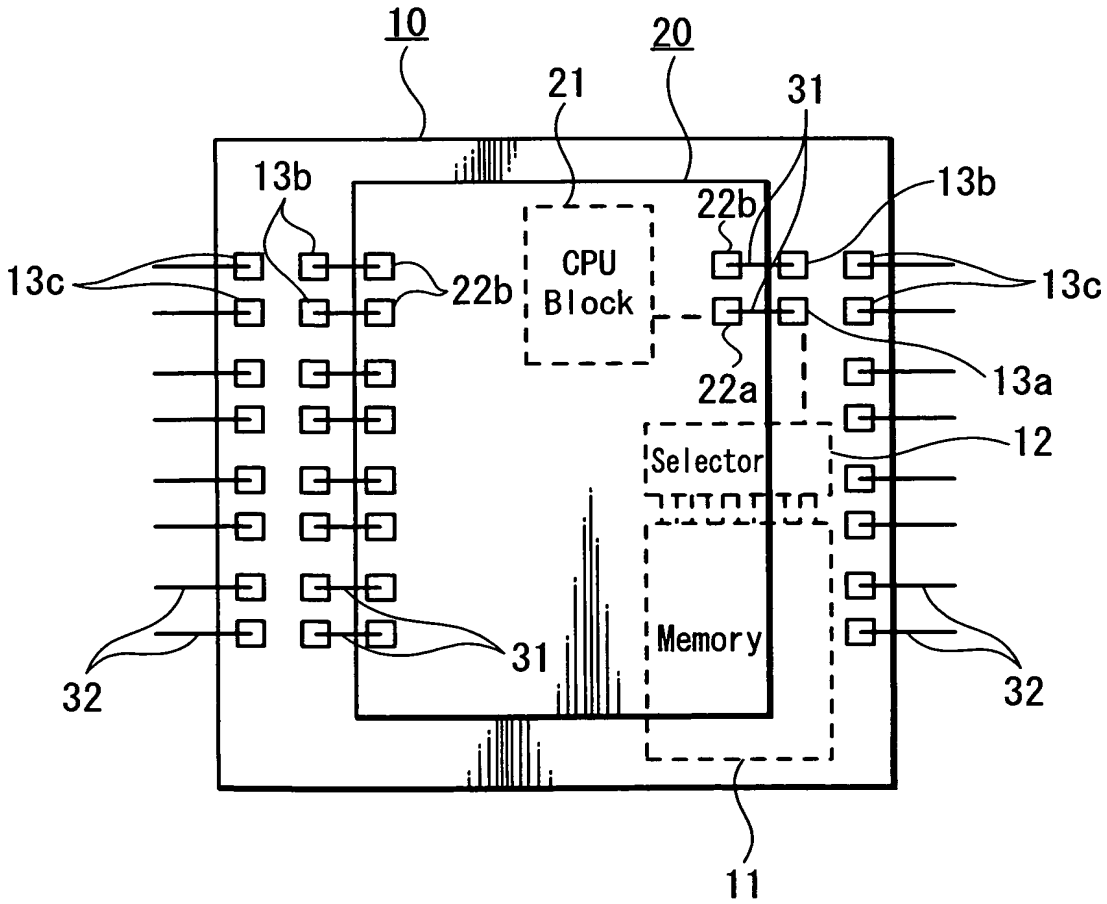
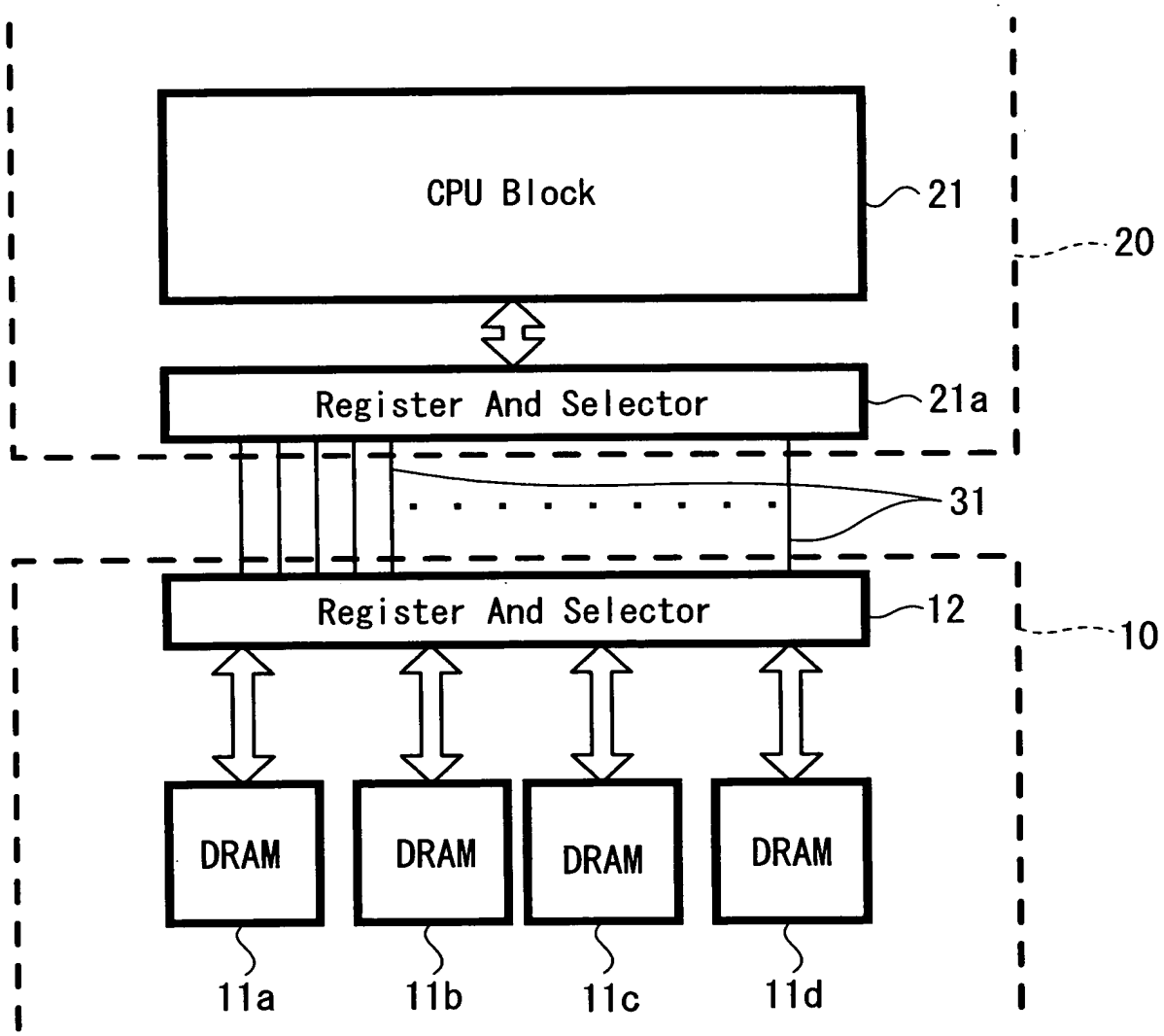

20
31
23
31
22b
21
22a
13a
13c
13b
14
13c
32
32
10
11, 12

FIG. 6


10
20
21
31
13b
13b
22b
CPU
Block
13c
22b
13c
22a
13a
Selector
12
Memory
31
32
32
11

10/553466

FIG. 7



DESCRIPTION OF REFERENCE NUMERALS

10.....FIRST CHIP

11, 11a to 11d.....DRAM

12.....REGISTER AND SELECTOR

13a to 13c.....PAD

20.....SECOND CHIP

21.....CPU BLOCK

21a.....REGISTER AND SELECTOR

22a, 22b.....PAD

31.....WIRE

100.....FIRST CHIP

101 to 105.....INTERNAL WIRING

111 to 114.....DRAM

121,122.....ELECTRODE HAVING MICRO BUMP

131.....PAD

200.....SECOND CHIP

201 to 105.....INTERNAL WIRING

210.....CPU BLOCK

211.....CPU

212.....SRAM

213.....INTERFACE

214.....ANALOGUE/DIGITAL CONVERTER

221,222.....ELECTRODE HAVING MICRO BUMP

301.....WIRE